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In re the Application of

Daniel CHATROUX et al.

Application No.: New U.S. National Stage of PCT/FR2003/003449

Filed: May 11, 2005

Docket No.: 123886

For: INTEGRATED CIRCUIT COMPRISING SERIES-CONNECTED SUBASSEMBLIES

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Relevance of the English language references 1-2 and 10 is discussed in the present specification.
- ☒ 3. The references 1-10 were cited in the International Search Report. An English language version of the International Search Report is attached for the Examiner's information.
- ☒ 4. In accordance with 37 CFR §1.98(a)(2)(ii), copies of any U.S. patents and patent application publications are not attached.

Respectfully submitted,



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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 123886		APPLICATION NO. New U.S. National Stage of PCT/FR2003/003449	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANTS Daniel CHATROUX et al.			
				FILING DATE May 11, 2005			

  

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1.	US 5,703,790	12/30/1997	FARWELL		
	2.	US 5,486,783	01/23/1996	BAUMERT et al.		
	3.	US 5,894,508	04/13/1999	KIM		
	4.	US 6,262,493 B1	07/17/2001	GARNETT		
	5.	US 6,308,279 B1	10/23/2001	TOLL et al.		
	6.	US 4,730,268	03/08/1988	MARIN		
	7.	US 5,877,928	03/02/1999	MAKINO		
	8.	US 6,229,360 B1	05/08/2001	MIZUNO et al.		

  

FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
	9.	EP 0 638 858 A1	02/15/1995	EUROPE		

  

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
10.	S.C. Tan et al.; "Low Power CMOS Level Shifters by Bootstrapping Technique"; Electronic Letters, IEE Stevenage, GB, Vol. 38, No. 16, pages 876-878; August 1, 2002

  

EXAMINER	DATE CONSIDERED
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Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date: May 11, 2005